

## Description

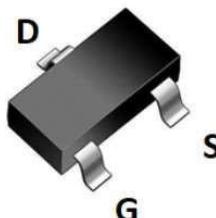
### PECJ N-channel Enhancement Mode Power MOSFET

#### Features

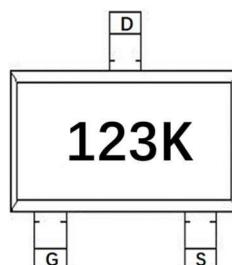
- $V_{DS}=100V$ ,  $I_D=0.17A$   
 $R_{DS(ON)} < 6\Omega$  @  $V_{GS} = 10V$
- $R_{DS(ON)} < 9\Omega$  @  $V_{GS} = 4.5V$
- High Power and Current Handling Capability
- Lead Free Product is Acquired
- Surface Mount Package
- ESD Protected: 1500V

#### Application

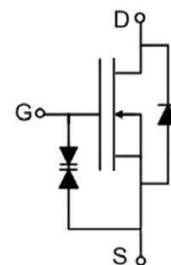
- Battery Protection
- Load Switch
- Power Management



SOT-23 top view



Marking and pin Assignment



Schematic diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
123K	PECJ123K	TAPING	SOT-23	13inch	4000	40000

## Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		50	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ C$	0.17	A
		$T_A = 100^\circ C$	0.11	
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		0.68	A
$P_D$	Power Dissipation	$T_A = 25^\circ C$	0.35	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		357	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ C$

## Electrical Characteristics ( $T_J=25^\circ\text{C}$ unless otherwise specified)

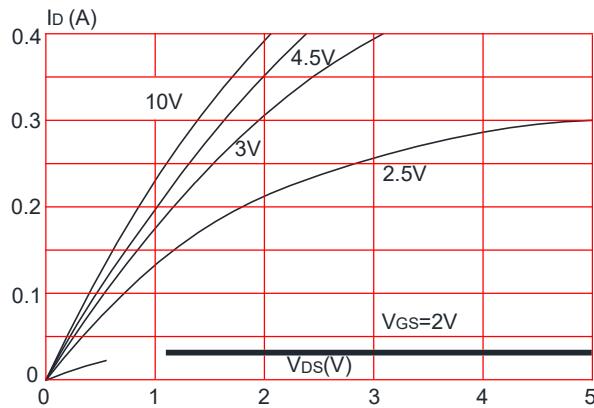
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	100	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=100\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	-	-	$\pm 10$	$\mu\text{A}$
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1.5	2	2.5	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$ , $I_D=0.5\text{A}$	-	4	6	$\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=0.2\text{A}$	-	6	9	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	-	60	pF
$C_{oss}$	Output Capacitance		-	-	15	pF
$C_{rss}$	Reverse Transfer Capacitance		-	-	6	pF
$Q_g$	Total Gate Charge	$V_{DS}=30\text{V}$ , $I_D=0.2\text{A}$ , $V_{GS}=10\text{V}$	-	-	2	nC
$Q_{gs}$	Gate-Source Charge		-	-	0.25	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	-	0.4	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=30\text{V}$ , $I_D=0.1\text{A}$ , $R_{\text{GEN}}=50\Omega$ , $V_{GS}=10\text{V}$ ,	-	-	8	ns
$t_r$	Turn-on Rise Time		-	-	8	ns
$t_{d(off)}$	Turn-off Delay Time		-	-	13	ns
$t_f$	Turn-off Fall Time		-	-	6	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	0.17	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	0.68	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S=0.2\text{A}$	-	-	1.2	V

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

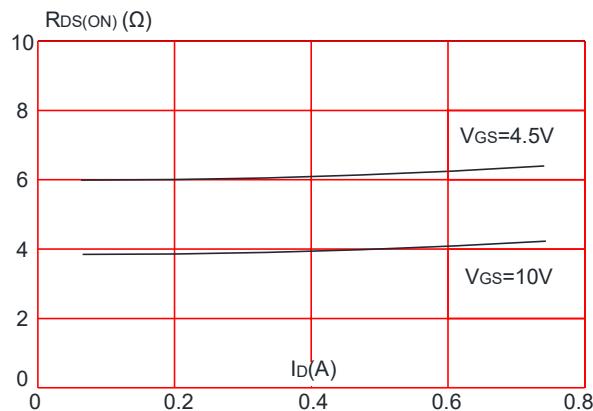
2. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$

## Typical Performance Characteristics

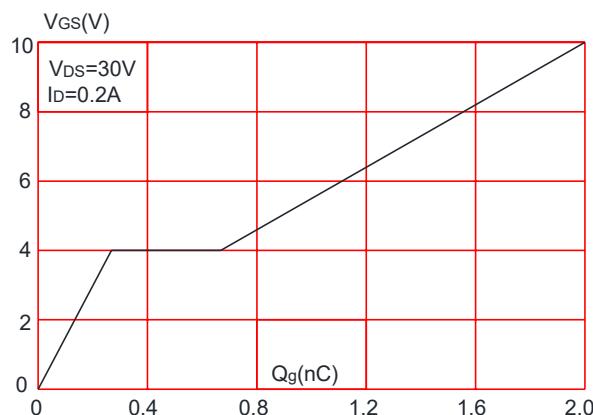
**Figure 1:** Output Characteristics



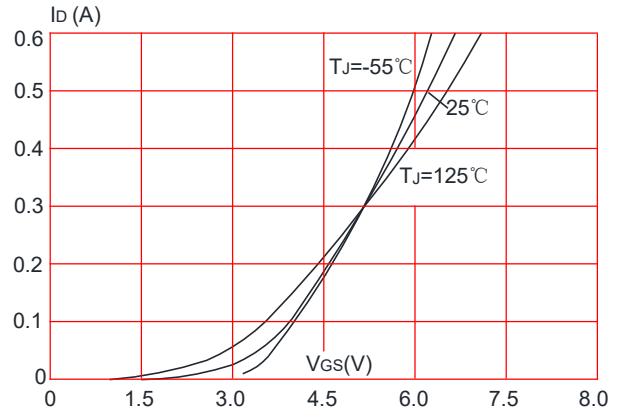
**Figure 3:** On-resistance vs. Drain Current



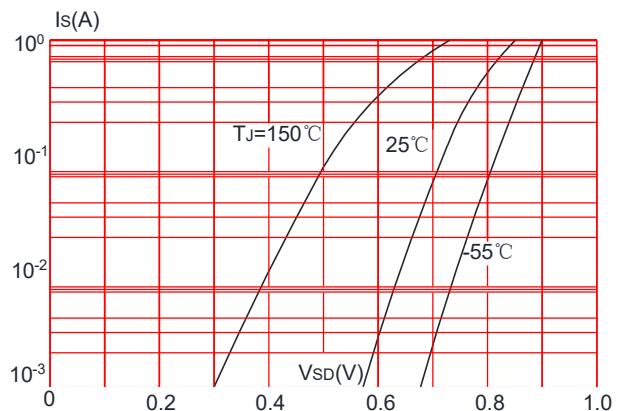
**Figure 5:** Gate Charge Characteristics



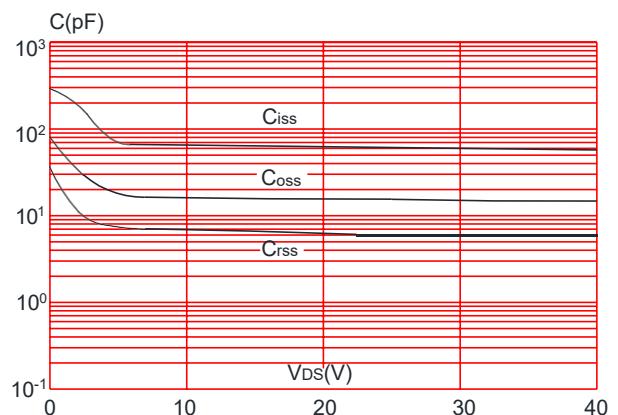
**Figure 2:** Typical Transfer Characteristics



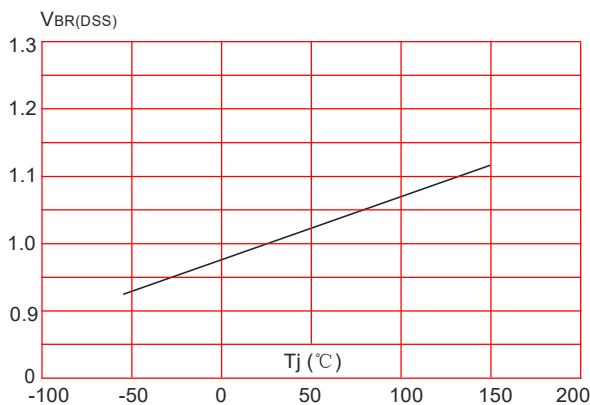
**Figure 4:** Body Diode Characteristics



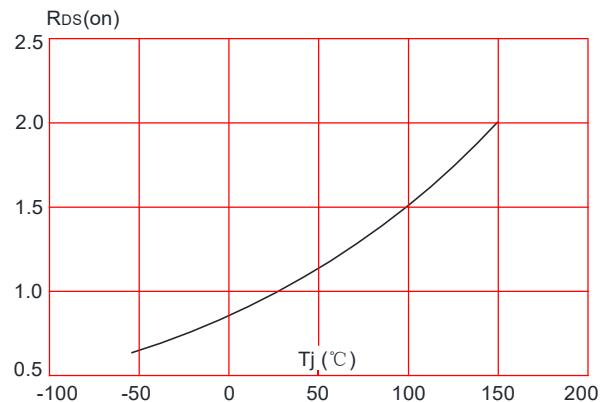
**Figure 6:** Capacitance Characteristics



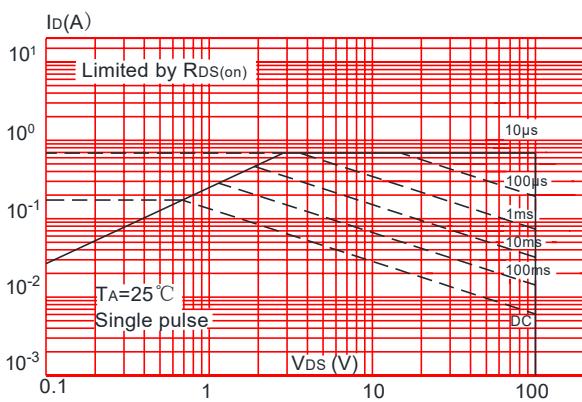
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



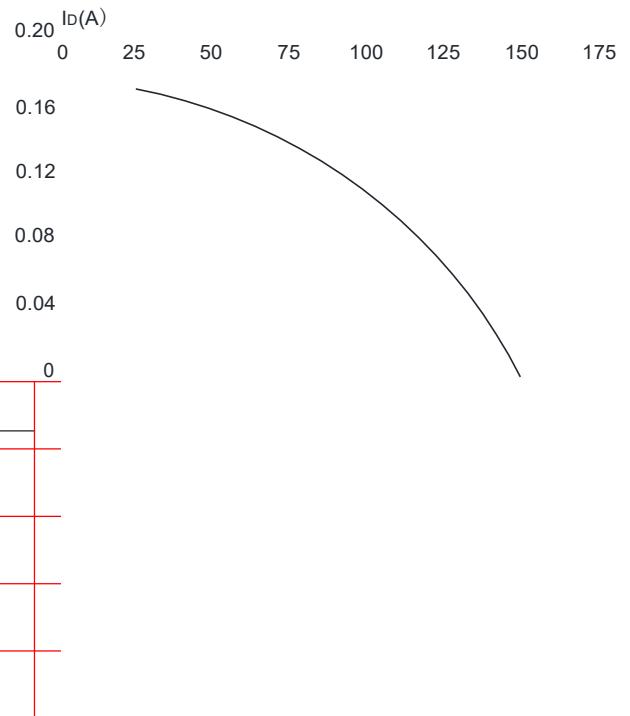
**Figure 8:** Normalized on Resistance vs. Junction Temperature



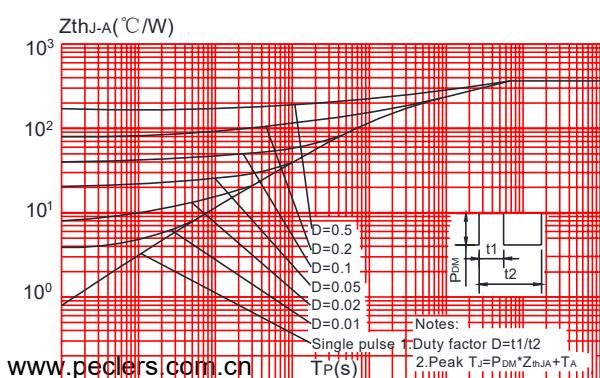
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Version :1.0

$10^{-1}$   
 $10^{-6}$      $10^{-5}$      $10^{-4}$      $10^{-3}$      $10^{-2}$      $10^{-1}$      $10^0$      $10^1$

## Test Circuit

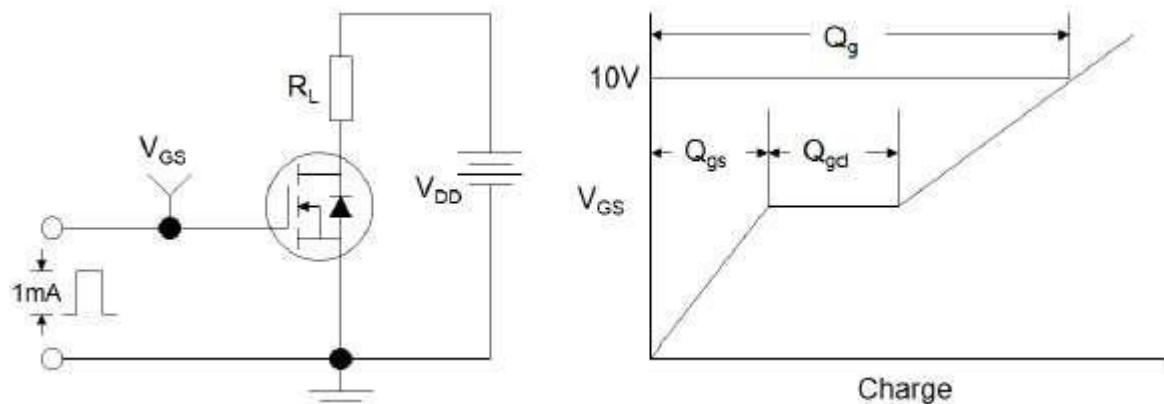


Figure1:Gate Charge Test Circuit & Waveform

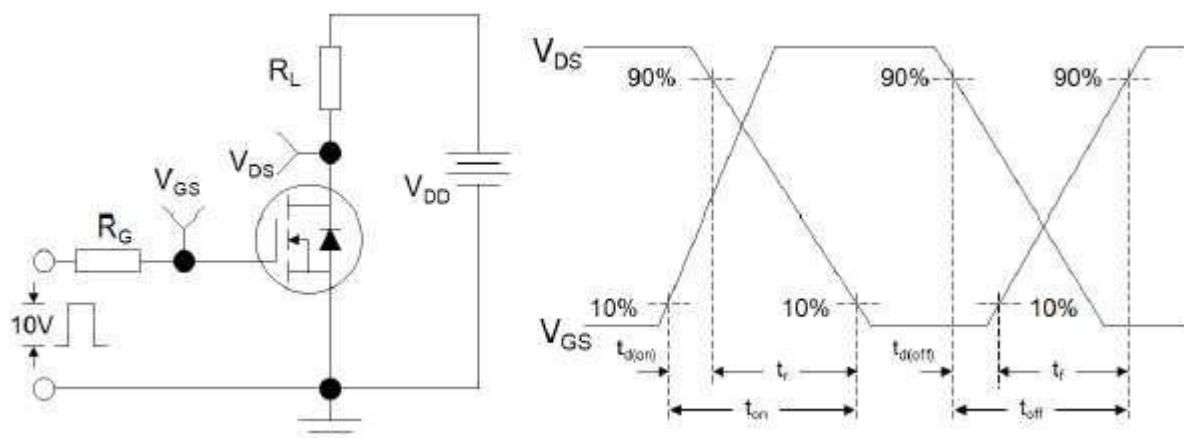


Figure 2: Resistive Switching Test Circuit & Waveforms

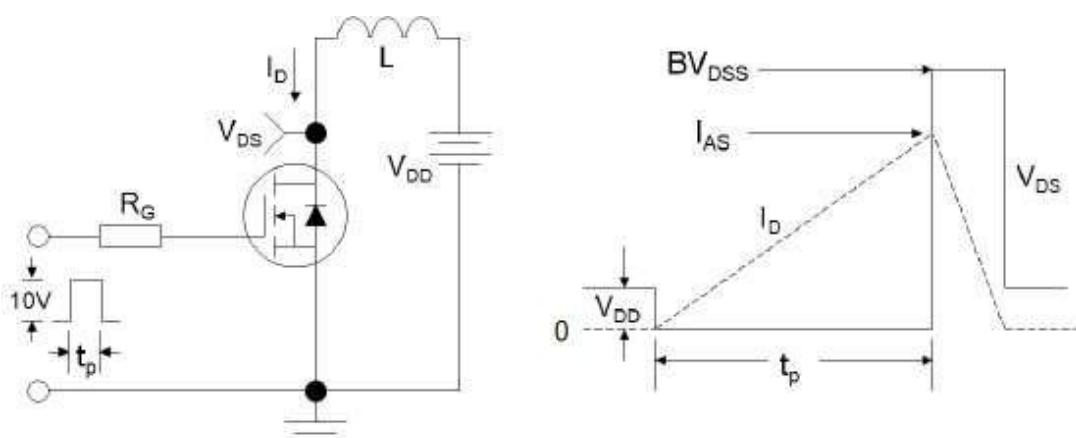
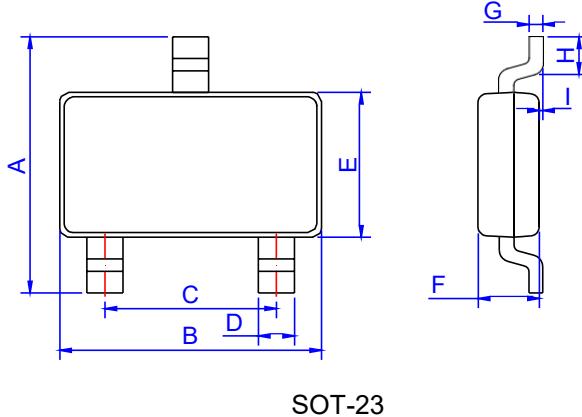


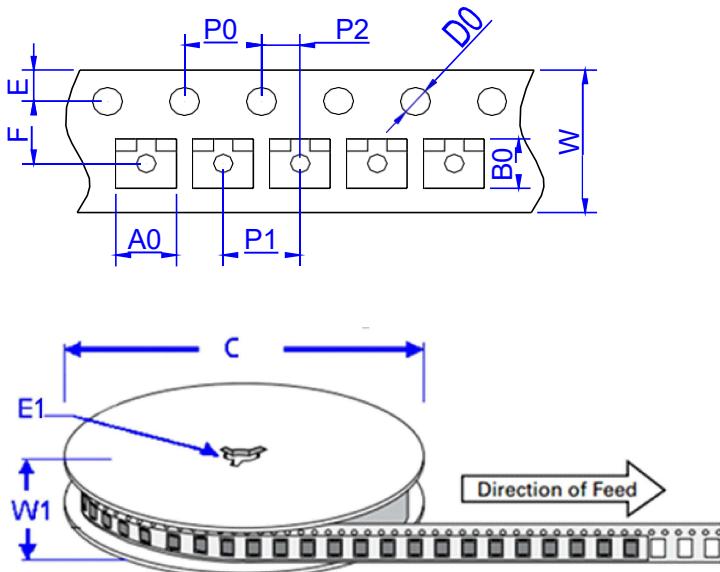
Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

## Package Mechanical Data-SOT-23



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.30	2.40	2.50	0.091	0.095	0.098
B	2.80	2.90	3.00	0.110	0.114	0.118
C	1.90 REF			0.075 REF		
D	0.35	0.40	0.45	0.014	0.016	0.018
E	1.20	1.30	1.40	0.047	0.051	0.055
F	0.90	1.00	1.10	0.035	0.039	0.043
G		0.10	0.15		0.004	0.006
H	0.20			0.008		
I	0		0.10	0		0.004

## Package Information-SOT-23



Ref.	Dimensions	
	Millimeters	Inches
A0	$3.15 \pm 0.3$	$0.124 \pm 0.012$
B0	$2.77 \pm 0.3$	$0.109 \pm 0.012$
C	178	7.0
D0	$1.50 \pm 0.1$	$0.059 \pm 0.004$
E	$1.75 \pm 0.2$	$0.069 \pm 0.008$
E1	$13.3 \pm 0.3$	$0.524 \pm 0.012$
F	$3.5 \pm 0.2$	$0.138 \pm 0.008$
P0	$4.00 \pm 0.2$	$0.157 \pm 0.008$
P1	$4.00 \pm 0.2$	$0.157 \pm 0.008$
P2	$2.00 \pm 0.2$	$0.079 \pm 0.008$
W	$8.00 \pm 0.2$	$0.315 \pm 0.008$
W1	$11.5 \pm 1.0$	$0.453 \pm 0.039$